

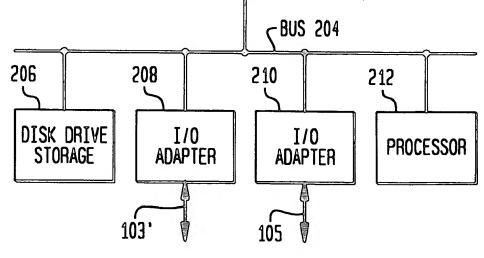
5,946,380

FIG. 2

CONTROL PROCESSOR 102⊳

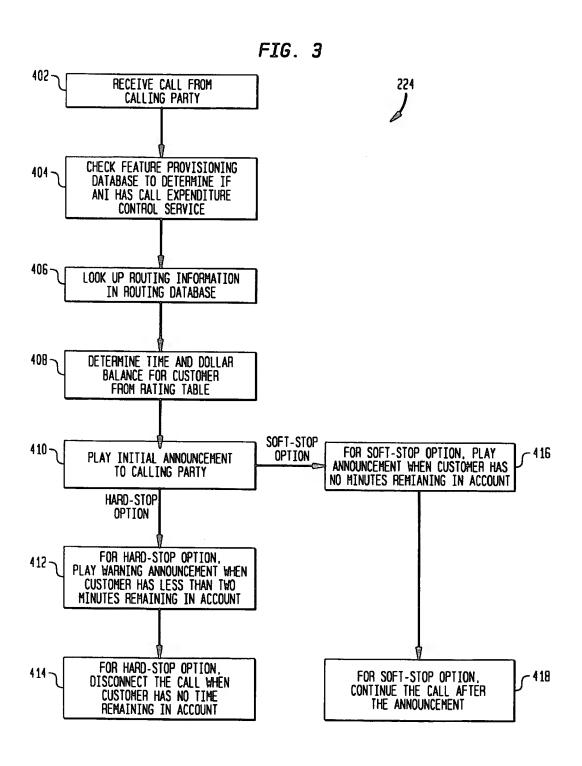
MEMORY 202

CALL EXPENDITURE CONTROL PROGRAM 224 RATING TABLE (TIME AND DOLLAR BALANCE) 230 ANNOUNCEMENT DATABASE 232 VOICE RESPONSE UNIT CONTROL PROGRAM 222 NETWORK SWITCH CONTROL PROGRAM 220

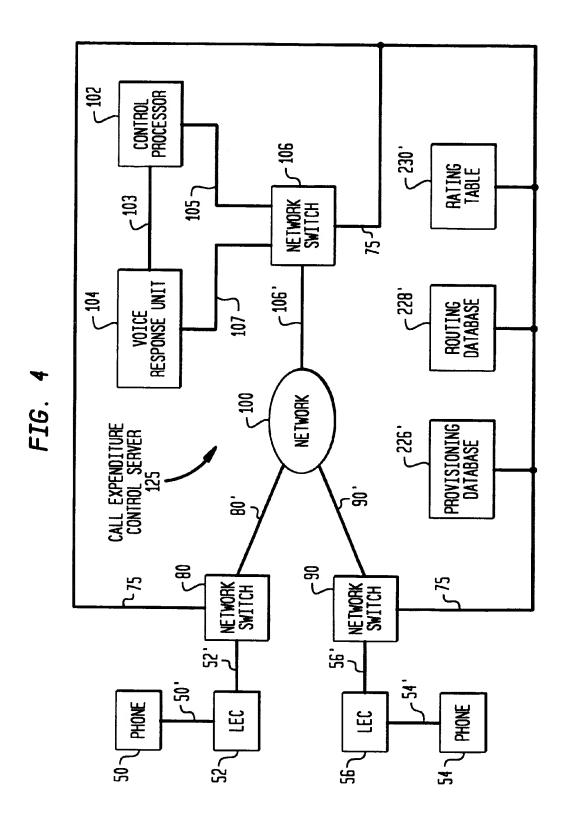


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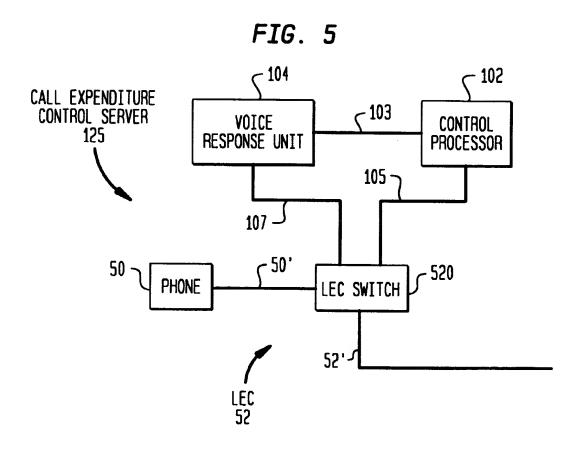








Aug. 31, 1999



DOUBLE CREACEL

Pooteno ottoni

FIG. 6 700 r 707 702ح 125 CTRL SERV **PSTN ~125**° CTRL SERV 706 706' 708 \ HLR ~70B' MSC MSC HLR -710' VLR BSC > 704 √701ⁿ 701-BS BS BS $\textbf{U}_1 \, \ldots \, \textbf{U}_n$ $v_1 \dots v_n$ $\textbf{U}_1 \ ... \ \textbf{U}_n$ $\textbf{U}_1 \, \ldots \, \textbf{U}_n$